RAMP BUFFER SCHEMATIC

- **Ramp generator**
- **Analog buffers to 5 channels**
Simulation with $C_{ext} = 10\text{pF}$, ramp current $= 5\text{uA}$

buffered ramp output keeps linearity $\sim 90\text{-}940\text{mV}$

possible to tweak $C_{ext}$, ramp current to improve this?