PSEC-3 mezzanine card test updates
Buffer (gain=1) on chan4 (3GHz)

mistake in schematic for voltage follower config.

← Board hack (also had to include -1V Vss – blue wire)
Buffer (gain=1) on chan4

Once board corrected, still issues with buffer (oscillating)
Tried several improvements, still not fixed

Several subsequent readouts shown:
Ped readout, all chans
Ped readout, all chans

- **Cross-talk**
- **Oscillating buffer**

Channels:
- chan1
- chan2
- chan3
- chan4
try channel 1 (2 rdouts shown)

Unknown discontinuities around middle of sampling array in chans 1 & 2...?
Still can do rough BW measurement using amplitudes